

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



**In re U.S. Patent Application of**

**YAMAMOTO et al.**

**Application Number: 10/817,004**

**Filed: April 5, 2004**

**For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND MANUFACTURING METHOD THEREOF**

**Attorney Docket No. HITA.0536**

**Art Unit 2826**

**Examiner Ahmed N. Sefer**

**Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**

**AMENDMENT AND RESPONSE**

Dear Sir:

This is in response to the Office Action mailed on August 11, 2006, the period of response to which is set to expire on January 11, 2007, with the payment of a two-month extension fee submitted herewith. Please amend the above-referenced application as follows: